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جامعة المنوفية كلية الهندسة الإلكترونية

قسم هندسة الالكترونيات و الاتصالات الكهربية



**Department offering the program**: Department offering the course:

Electronics and Electrical Communications Engineering Industrial electronics and Control Engineering

No.

## **Course Specification**

1-Course basic information :				
Course Code: ACE 216	Course Title:	Academic year: 2015-2016		
Department requirement	Power Electronics	Level (2) – Semester : 1 <sup>st</sup>		
Field: Basic Eng. Science	Teaching hours: Lecture [	2] - Tutorial[1] - Lab[0]		

<ul> <li>2- Course objectives         <ol> <li>To introduce students to power electronics and types of power electronic circ circ objectives</li> <li>To enhance student ability to demonstrate Power semiconductor devices, construction, operation, ratings and circuit applications.</li> <li>To teach students resistance and RC triggering circuits for thyristors.</li> <li>To acquire students a good knowledge for the design of firing circuits, Switch circuits and protection.</li> <li>To teach students single phase half wave rectifiers.</li> <li>To enhance student ability to demonstrate single phase and three phase contrarectifiers             <li>To introduce students to commutation circuits.</li> </li></ol></li></ul> <li>Intended Learning Outcomes         <ul> <li>(A.1 Explain concepts and theories of mathematics and sciences, appropriate to the Power Electronics Circuits.</li> <li>A.3 Define characteristics of engineering materials related to the Power Electronics Circuits.</li> <li>A.3 Define characteristics of engineering materials related to the Power Electronics Circuits.</li> </ul> </li> <li>A.3 Define characteristics of engineering materials related to the Power Electronics Circuits.</li> <li>A.3 Define characteristics of engineering the electronics Circuits.</li> <ul> <li>A.3 Define characteristics of engineering the electronics Circuits.</li> <ul> <li>A.4 Explain concepts and theories of sciences, appropriate to the Triggering circuit.</li> <li>A.5 Explain concepts and theories of sciences, appropriate to the SCR, Triac, Gto, UJT, PUT and D circuits, and applications.</li> <li>A.5 Explain concepts and theories of sciences, appropriate to the Superson and theories of sciences, appropriate to the Superson and theories of sciences, appropriate to the Superson and theories of sciences, appropriate to the R</li></ul></ul>	ning off
construction, operation, ratings and circuit applications.         3. To teach students resistance and RC triggering circuits for thyristors.         4. To acquire students a good knowledge for the design of firing circuits, Switch circuits and protection.         5. To teach students single phase half wave rectifiers.         6. To enhance student ability to demonstrate single phase and three phase contructifiers         7. To introduce students to commutation circuits.         3- Intended Learning Outcomes (ARS)         A.1 Explain concepts and theories of mathematics and sciences, appropriate to the Power Electronics Circuits.         A.1 Explain concepts and theories of sciences, appropriate to the Power semiconductor devices.         A1.2 Explain concepts and theories of sciences, appropriate to the Power semiconductor devices.         A1.3 Explain concepts and theories of sciences, appropriate to the SCR, Triac, Gto, UJT, PUT and D circuits, and applications.	Č.
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6. To enhance student ability to demonstrate single phase and three phase contrarectifiers         7. To introduce students to commutation circuits.         3- Intended Learning Outcomes (ARS)       Course (ILOs):         A.1 Explain concepts and theories of mathematics and sciences, appropriate to the Power Electronics Circuits.       A1.1 Explain concepts and theories of sciences, appropriate to the Power semiconductor devices.         A1.2 Explain concepts and theories of sciences, appropriate to the Power semiconductor devices.       A1.2 Explain concepts and theories of sciences, appropriate to the SCR, Triac, Gto, UJT, PUT and D circuits, and applications.         A1.3 Explain concepts and theories of sciences,	
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A1.6 Explain concepts and theories of sciences,	
appropriate to the Recovery of trapped energy	
appropriate to the Recovery of trapped energy.	
A1.7 Explain concepts and theories of sciences,	
appropriate to the Single and three phase controlled	
rectifiers.	
A3.1 Define characteristics of engineering materials	
related to the SCR, Triac, Gto, UJT, PUT and Diac	
devices.	
A3.2 Define characteristics of engineering materials	
related to the firing circuits.	
A3.3 Define characteristics of engineering materials	
related to the Switching off and protection circuits.	

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		A3.4 Define characteristics of engrelated to the Heat sinks.	ineering materials	
	A4) Explain Principles of design including elements design, process and/or a system related to Power Electronics Circuits.	<ul> <li>A4.1) Explain principles of design</li> <li>Gto, UJT, PUT and Diac circuits.</li> <li>A4.2) Explain principles of design</li> <li>circuits.</li> <li>A4.3) Explain principles of design</li> </ul>	n related to Triggering	
	A8) Describe Current engineering technologies as related to Power Electronics Circuits.	<ul> <li>A8.1) Describe current engineering technologies as related to Power semiconductor devices construction.</li> <li>A8-2) Describe current engineering technologies as related to Triggering circuits.</li> <li>A8.3) Describe current engineering technologies as related to Switching off and protection circuits.</li> </ul>		
<b>B- Intellectual Skills</b>	B.5 Assess and evaluate the characteristics and performance of components, systems and processes.	to Switching off and protection circuits. B5.1 Assess and evaluate the characteristics and performance of SCR, Triac, Gto, UJT, PUT and Diac. B5.2 Assess and evaluate the characteristics and performance of Triggering circuits. B5.3 Assess and evaluate the characteristics and performance of firing circuits. B5.4 Assess and evaluate the characteristics and performance of Switching off and protection circuits. B5.5 Assess and evaluate the characteristics and performance of Recovery of trapped energy process. B5.6 Assess and evaluate the characteristics and performance of Single and three phase controlled rectifiers B5.7 Assess and evaluate the characteristics and performance of Single and three phase controlled rectifiers		
	B6) Investigate the failure of components, systems, and processes.	<ul> <li>B6.1 Investigate the failure of SC and Diac devices.</li> <li>B6.2 Investigate the failure of thy B6.3 Investigate the failure of Sw circuits.</li> </ul>	ristors.	
C- Professional Skills	C3) Create and/or re-design a process, component or system, and carry out specialized engineering designs.	C3.1 Create and/or re-design SCR and Diac. C3.2 Create and/or re-design Trig C3.3 Create and/or re-design firin C3.4 Create and/or re-design Swit circuits. C3.5 Create and/or re-design Sing controlled rectifiers.	gering circuits. g circuits. ching off and protection	
Ċ	C.12 Prepare and present technical reports.	C12.1 Prepare and present technic related to Power Electronics Circu		

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	D.3 Communicate	e effectively.	D3.1 Communicate effec	tively in class room with his		
cills			colleagues, and teaching	staff member.		
D- General Skills	D.6 Effectively manages tasks, time, and resources.		D6.1 Effectively manages tasks, time, and resources in tutorial, while writing a report and exam times.			
D- Ge	D.7 Search for information and engage in life-long self learning Power Electronics Circuits.			D7.1 Search for information and engage in life-long self learning in topics related to Power Electronics Circuits.		
5-7	Course Contents	with AC line voltage – Switching off and protection circuits– switched D source with different load circuits – Recovery of trapped energy – sing phase half wave rectifier with different load circuits and with electromotiv force (emf) – RL load circuit with freewheeling diode – Single phase an three phase controlled rectifiers with different load circuits and emf – He sinks specification – Commutation circuits.				
Learning Methods       • Tutorials.         • Research assignments.						
	<b>Feaching and</b>		tion of the office hours for	those students.		
	rning Methods disable students	• Give them sp				
IOF	uisable students	-	-	naterial at Lectures and tutorials.		
Official low cost special classes for developing student skills, arr by the faculty administration.				eloping student skins, arranged		
7- Student Assessment						
<b>a-</b> A	Assessment	- Weekly sheet	exercises at class room			
Ν	Methods	- Quizzes - Mid-term, and	final aroma	89 0 /		
<b>b</b> - 4	Assessment	- Exercise sheet		Weekly		
	edule	- Quizz-1:		Week no 5		
		- Mid-Term exa	ım:	Week no 8		
		- Quizz-2:		Week <u>no</u> 11		
		- Final – term e		Week <u>no</u> 16 to 18		
	Veighting of	- Class work an		15 %		
Ass	essment	- Mid-term example		15 % 70 %		
		- Final – term e	xamination: Total	<u>70 %</u> 100 %		
8. T	List of text books :	and references.	Total	100 /0		
	Course notes		ures notes prepared in the	form of a book authorized by		
a- (		the departmer		total of a book autionized by		
<b>b-</b> ]	Fext books	1. M. A. Rashic		wits, devices, and Applications ",		
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	2. V. Jagannathan, "Power Electronics : Devices and Circuits ",PHI			
	Learning Pvt. Ltd., 2011			
c- Recommended	1. Y. S. Lee and M. H. L.Chow " Power Electronics Handbook ", San			
books	Diego, CA : Academic Press, 2001.			
	2. R. G. Hoft, "Semiconductor Power Electronics ", New York : Van			
	Nostrand Reinhold, 1986.			
d- Periodicals, Web	• www.smpstech.com/books/booklist.htm			
sitesetc	• www.darnell.com			

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# Course contents - ILOs Matrix

Content Topics	Week	A- Knowledge & Understanding	Intellectual	C- Prof.and practical skills	D- General and transferable skills
Introduction to power electronics – Types of power electronic circuits - Power semiconductor devices, construction, operation, ratings, how to choose it, and circuits application	1-2	A1.1, A8.1	SKIIIS	C12.1	D3.1, D6.1, D7.1
SCR, Triac, Gto, UJT, PUT, Diac	3-5	A1.2, A3.1, A4.1	B5.1, B6.1	C3.1, C12.1	D3.1, D6.1, D7.1
Triggering circuits for thyristors, resistance trigger circuit – RC trigger circuits – UJT for trigger circuits – Diac and PUT to provide a pulse to trigger thyristors	6-7	A1.3, A4.2, A8.2	B5.2, B6.2	C3.2, C12.1	D3.1, D6.1, D7.1
Design of firing circuits	9	A1.4, A3.2, A4.3	B5.3	C3.3, C12.1	D3.1, D6.1, D7.1
Synchronizing a UJT pulse with AC line voltage –	10	A1.5	0	C12.1	D3.1, D6.1, D7.1
Switching off and protection circuits – switched DC source with different load circuits	11	A3.3, A8.3	B5.4, B6.3	C3.4, C12.1	D3.1, D6.1, D7.1
Recovery of trapped energy	12	A1.6,	B5.5	C12.1	D3.1, D6.1, D7.1
Single phase half wave rectifier with different load circuits and with electromotive force (emf) – RL load circuit with freewheeling diode – Single phase and three phase controlled rectifiers with different load circuits and emf	13-14	A1.7	B5.6	C3.5, C12.1	D3.1, D6.1, D7.1
Heat sinks specification – Commutation circuits.	15	A3.4	B5.7	C12.1	D3.1, D6.1, D7.1





Teaching and Learning Methods - ILOs Matrix

Teaching and Learning Methods	A- Knowledge & Understanding	B- Intellectual skills	C- Professional and practical skills	D- General and transferable skills
Lectures	A1,A3,A4,A8	B5, B6	C3, C12	D3
Tutorials.	A1,A3,A4,A8	B5, B6	C3, C12	D3, D6
Reports and assignments	A1,A3,A4,A8	B5, B6	C3, C12	D6, D7

### **Assessment Methods - ILOs Matrix**

Assessment Methods	A- Knowledge &	B- Intellectual skills	C- Professional and practical	D- General and transferable
	Understanding		skills	skills
Weekly sheet	A1,A3,A4,A8	B5, B6	C3	D3, D6, D7
exercises	× 1			1.6.1
Reports	A1,A3,A4,A8	B5, B6	C3, C12	D6, D7
Quizzes	A1,A3,A4,A8	B5, B6	C3	D6
Midterm, and Final Written exams	A1,A3,A4,A8	B5, B6	C3	D6

Authorized from department board at 15/05/2016 Authorized from college board at 05/06/2016

# **Course coordinator:**

Prof. Abdelazem S. Ibrahim

Head of Department: Prof. Fathi El-Sayed Abd El-Samie



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